Abstracts

A Cascaded-Impedance-Inverter Model of Wide-Band Frequency Triplers

J.C. Redd and K.L. Kotzebue. "A Cascaded-Impedance-Inverter Model of Wide-Band Frequency Triplers." 1971 G-MTT International Microwave Symposium Digest of Technical Papers 71.1 (1971 [MWSYM]): 96-98.

A charge-storage diode frequency tripler can be modeled as two cascaded impedance inverters. This approach has been used to design and construct a tripler at 1.06 GHz input frequency with a measured midband efficiency of 50% and a 3 dB band-width of 38%.

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